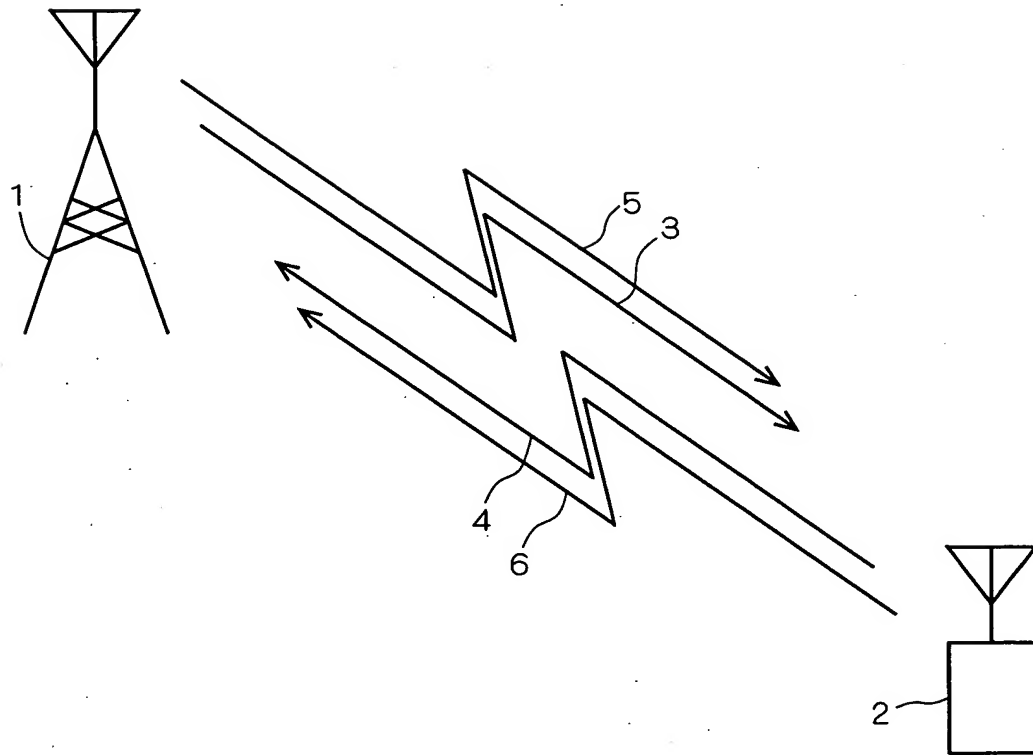


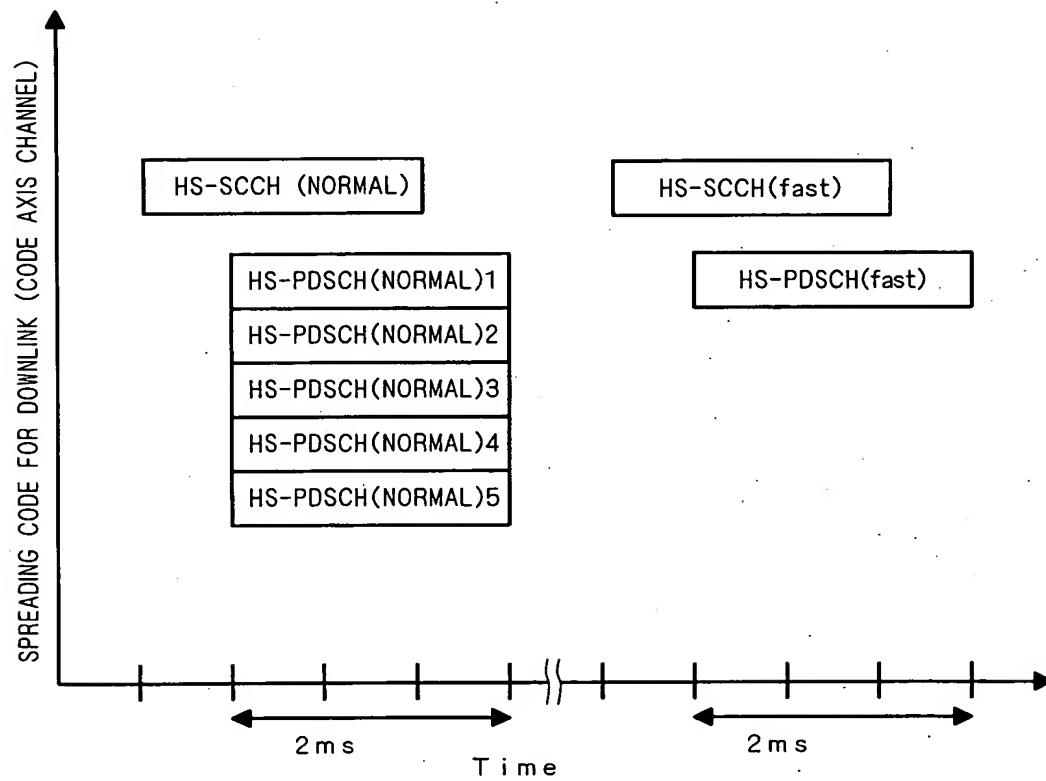
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FIG. 1



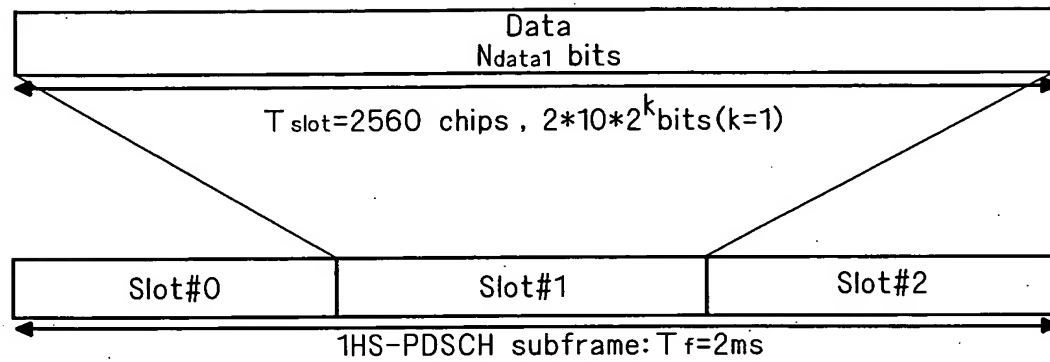
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FIG. 2



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FIG. 3



Slot format#1	Channel Bit Rate (kbps)	Channel Symbol Rate(ksps)	SF	Bits/HS- DSCH subframe	Bits/Slot	Ndata
2(QPSK)	60	30	128	120	40	40

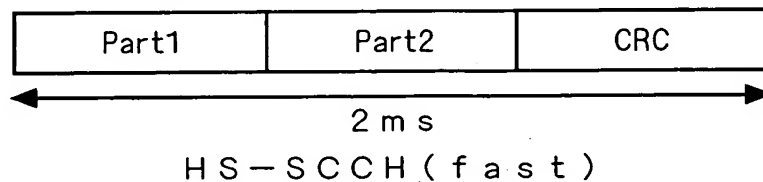
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FIG. 4

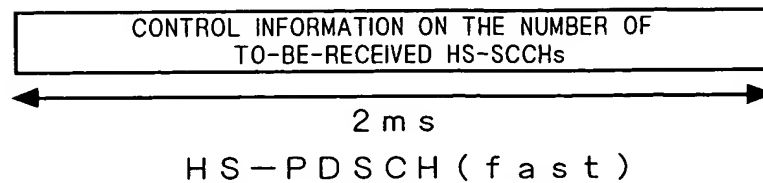
(A)

		Tree offset indicator(4 bits)															
		0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Cluster code Indicator (3 bits)	0(1/15)	1 1	1 2	1 3	1 4	1 5	1 6	1 7	1 8	1 9	1 10	1 11	1 12	1 13	1 14	1 15	15 1
	1(2/14)	2 1	2 2	2 3	2 4	2 5	2 6	2 7	2 8	2 9	2 10	2 11	2 12	2 13	2 14	14 2	14 1
	2(3/13)	3 1	3 2	3 3	3 4	3 5	3 6	3 7	3 8	3 9	3 10	3 11	3 12	3 13	13 3	13 2	13 1
	3(4/12)	4 1	4 2	4 3	4 4	4 5	4 6	4 7	4 8	4 9	4 10	4 11	4 12	12 4	12 3	12 2	12 1
	4(5/11)	5 1	5 2	5 3	5 4	5 5	5 6	5 7	5 8	5 9	5 10	5 11	11 5	11 4	11 3	11 2	11 1
	5(6/10)	6 1	6 2	6 3	6 4	6 5	6 6	6 7	6 8	6 9	6 10	10 6	10 5	10 4	10 3	10 2	10 1
	6(7/9)	7 1	7 2	7 3	7 4	7 5	7 6	7 7	7 8	7 9	9 7	9 6	9 5	9 4	9 3	9 2	9 1
	7(8/8)									8 8	8 7	8 6	8 5	8 4	8 3	8 2	8 1

(B)



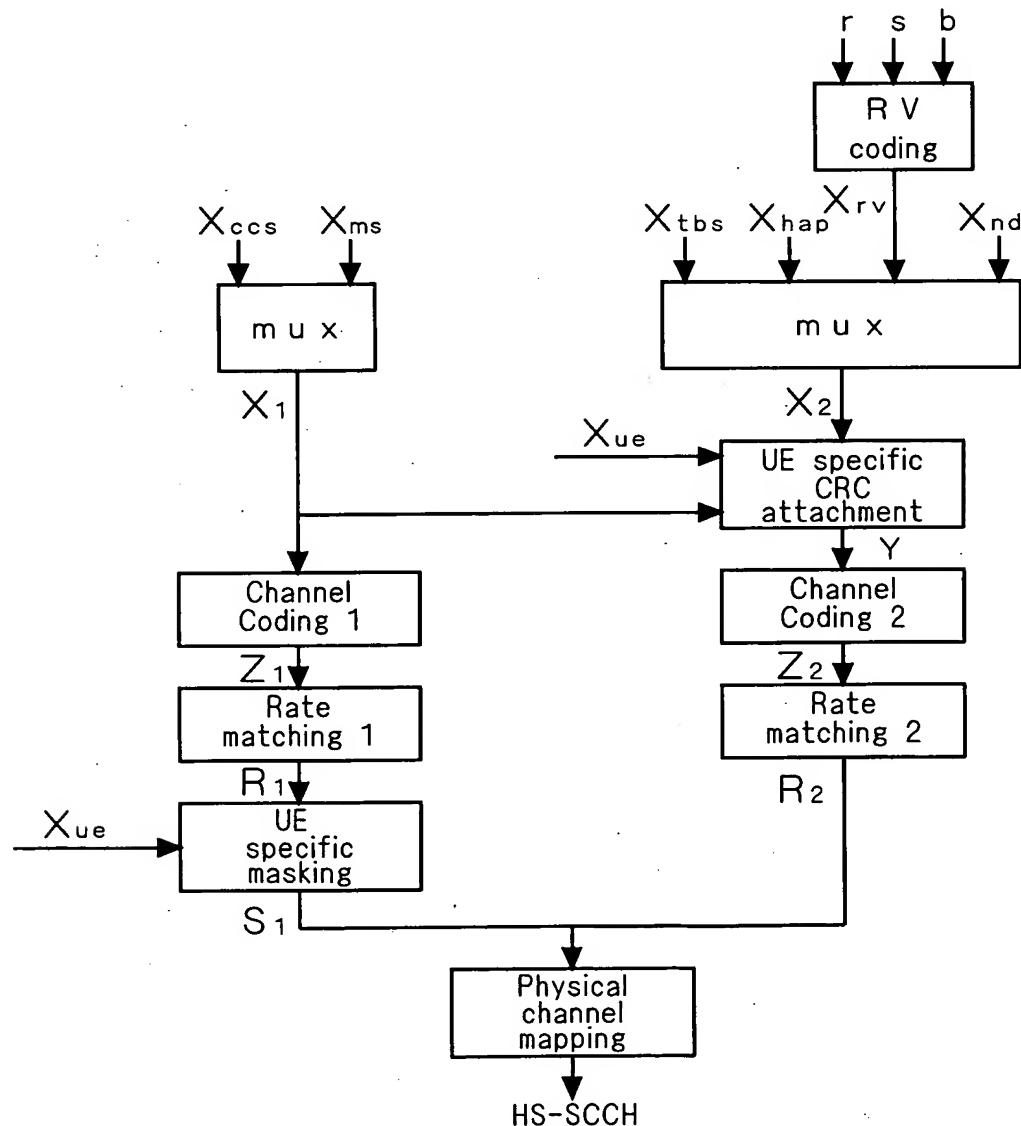
(C)



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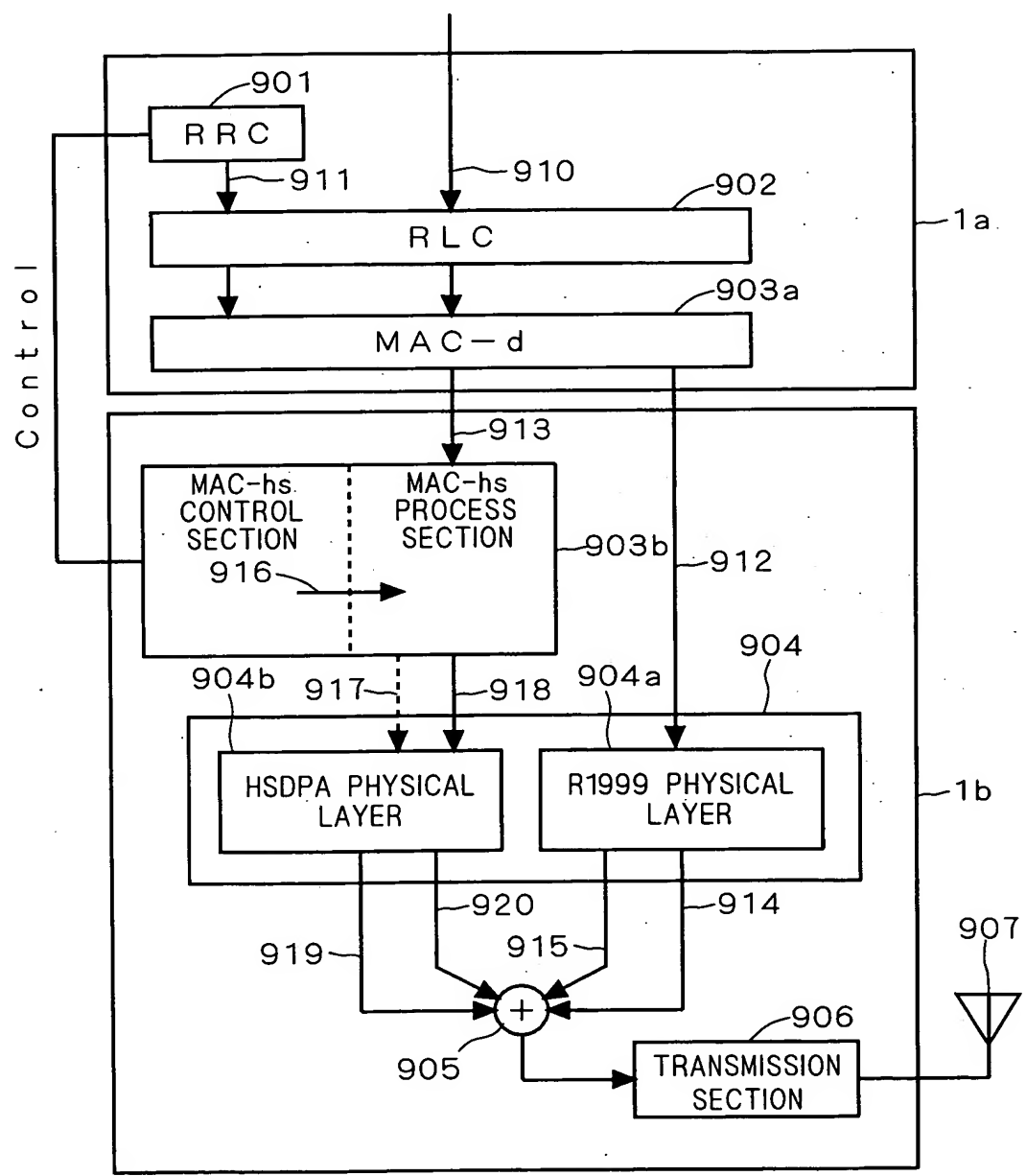
FIG. 5

- Channelization-code-set information or Fast signalling notification (7 bits): $X_{ccs,1}, X_{ccs,2}, \dots, X_{ccs,7}$
- Modulation scheme information(1 bit): $X_{ms,1}$
- Transport-block size information(6 bits): $X_{tbs,1}, X_{tbs,2}, \dots, X_{tbs,6}$
- Hybrid-ARQ process information(3 bits): $X_{hap,1}, X_{hap,2}, X_{hap,3}$
- Redundancy and constellation version(3 bits): $X_{rv,1}, X_{rv,2}, X_{rv,3}$
- New data indicator(1 bit): $X_{nd,1}$
- UE identity(16 bits): $X_{ue,1}, X_{ue,2}, \dots, X_{ue,16}$



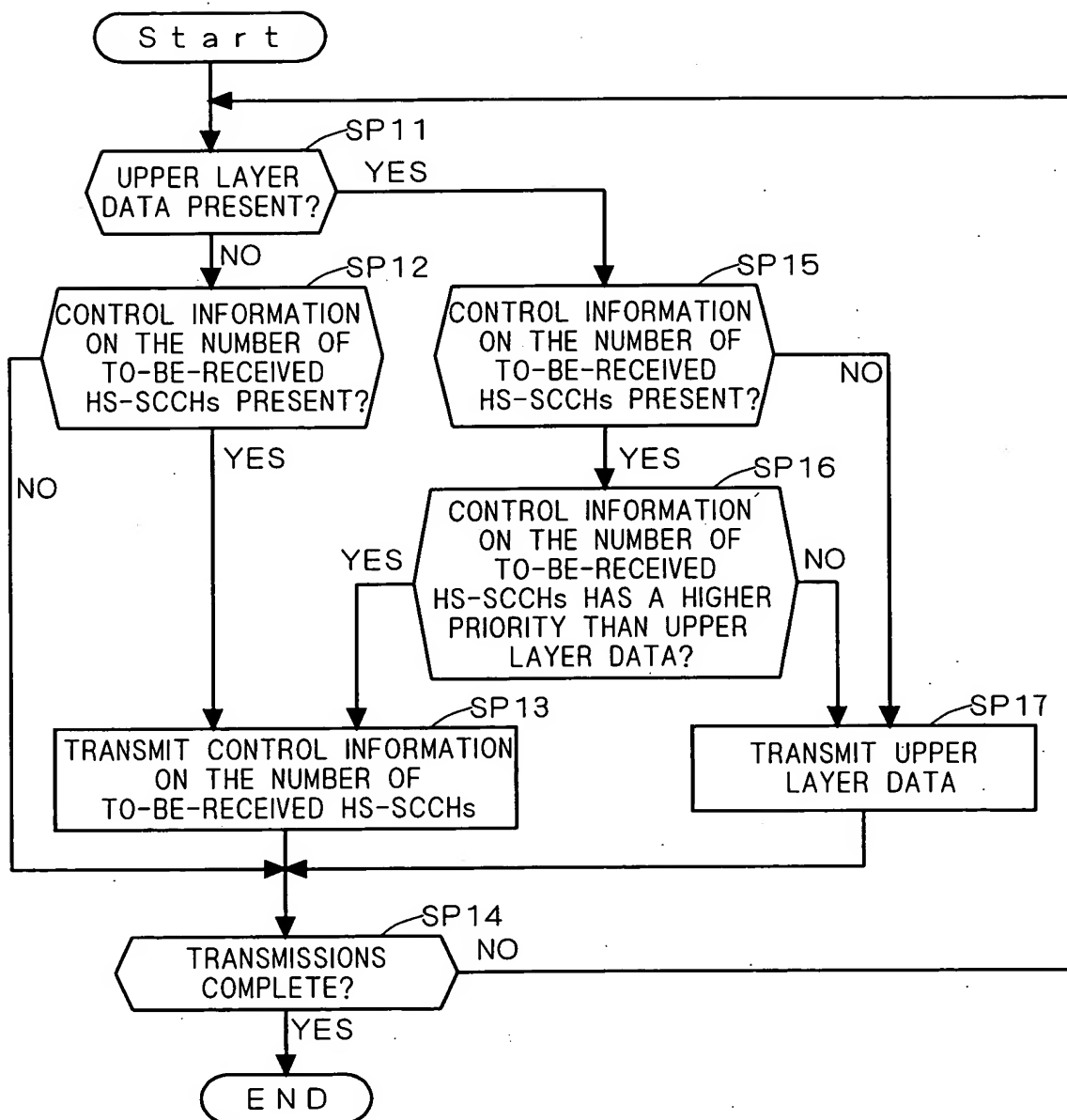
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FIG. 6



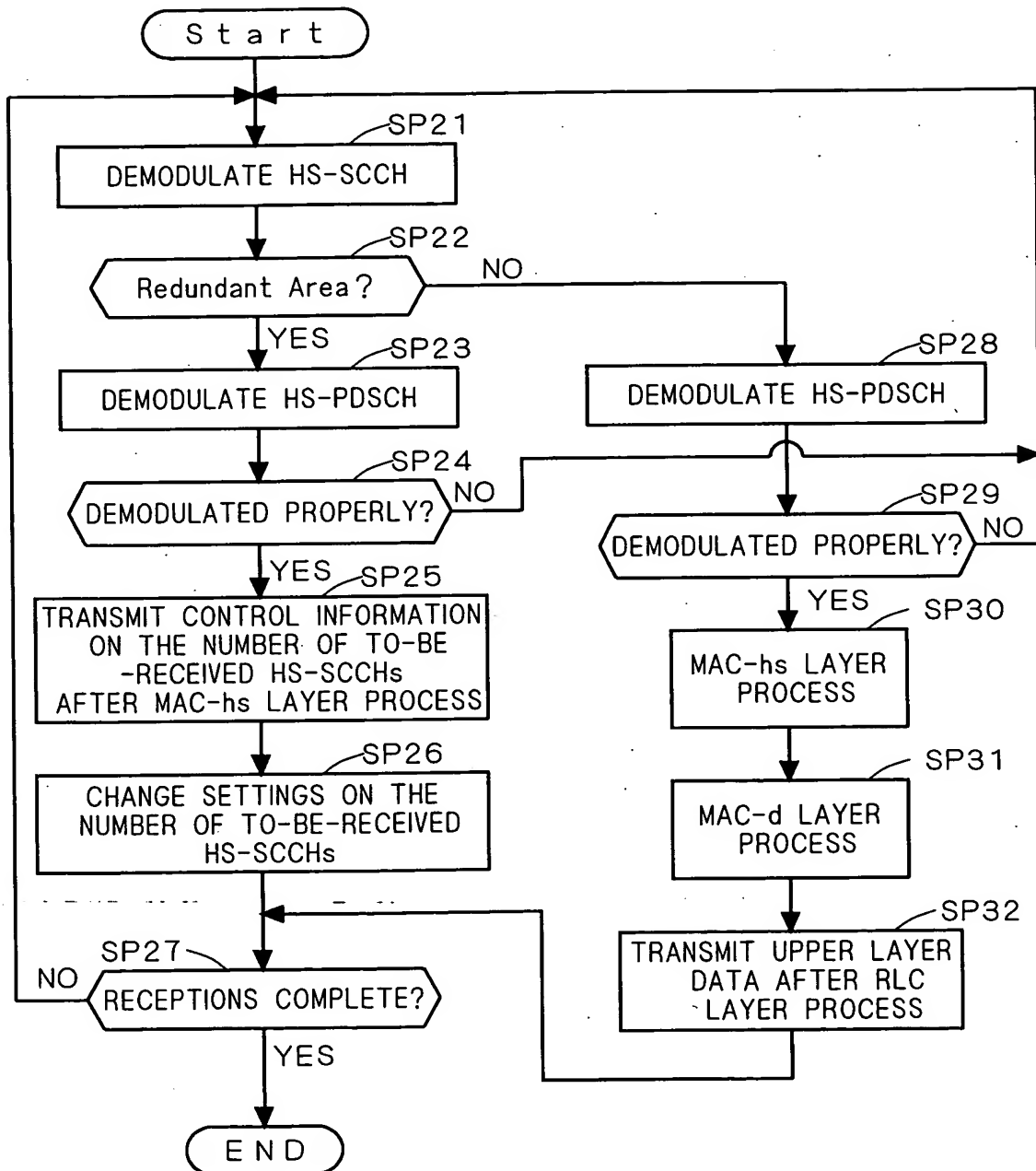
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FIG. 7



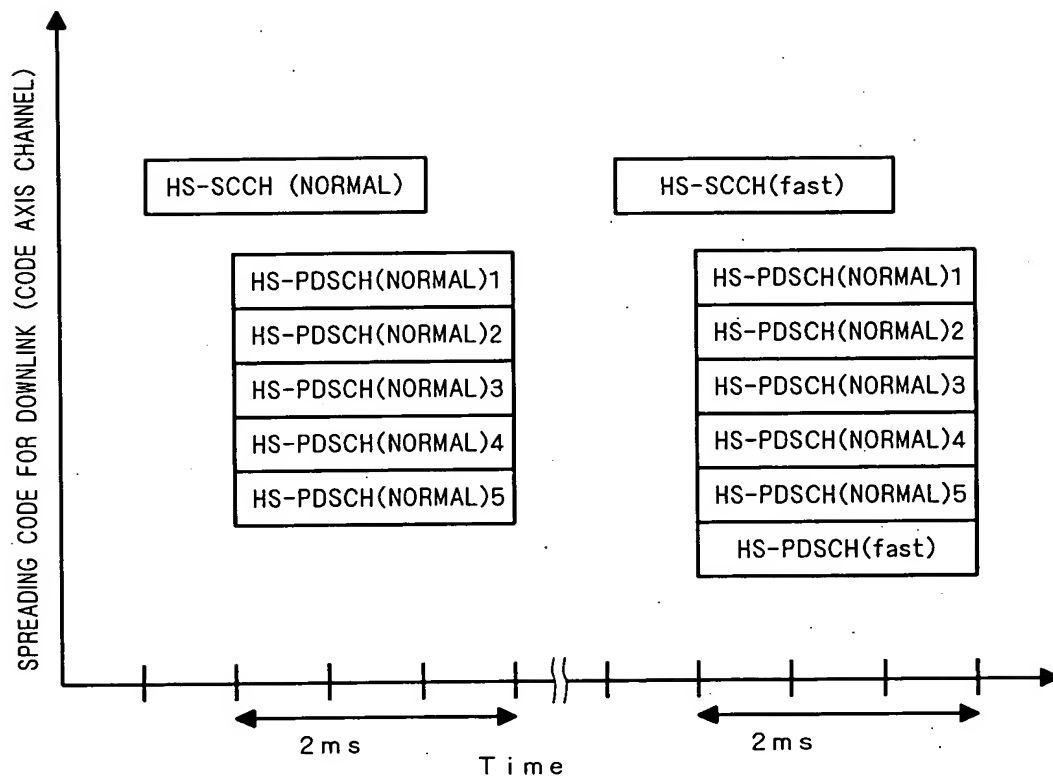
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FIG. 9



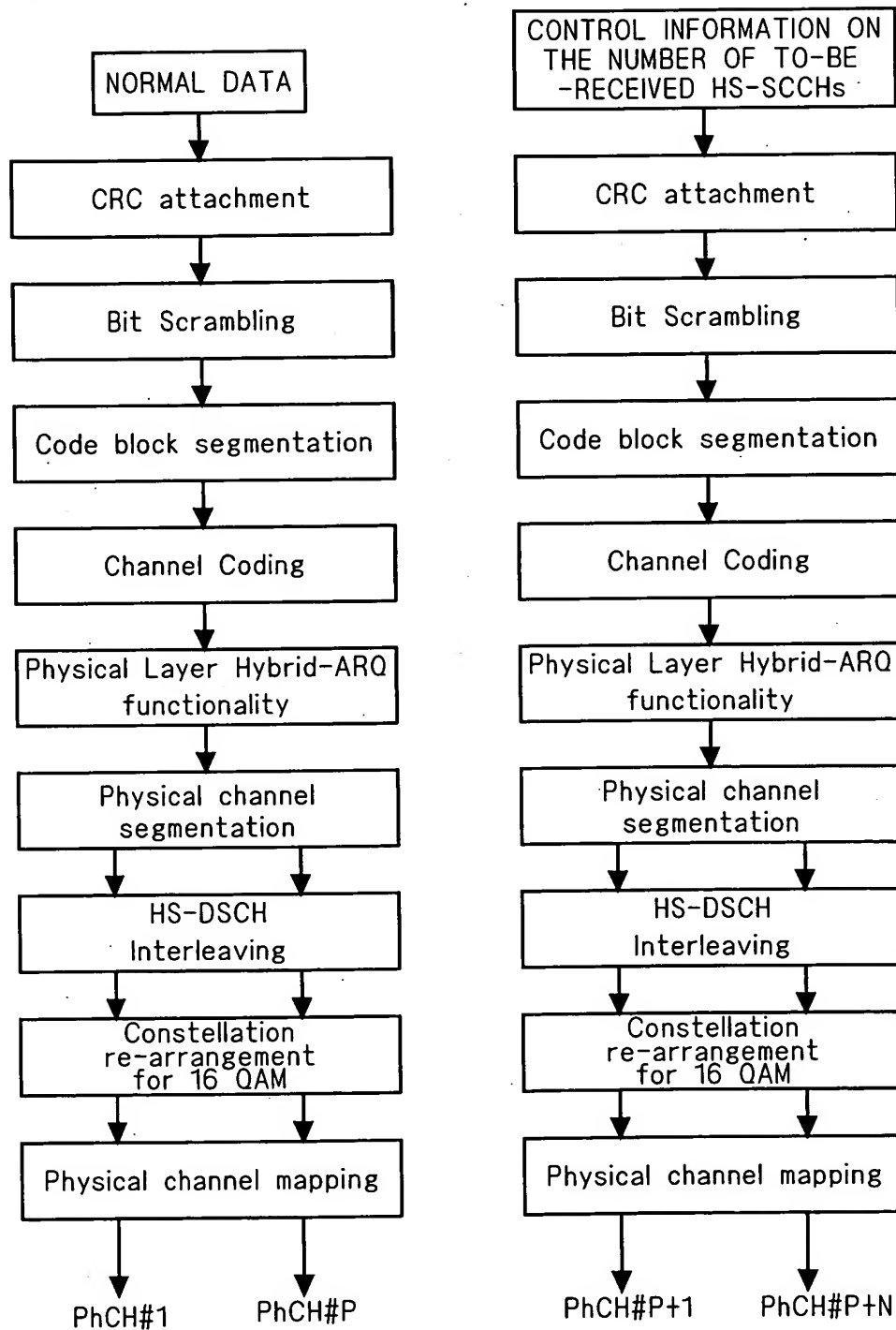
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FIG. 10



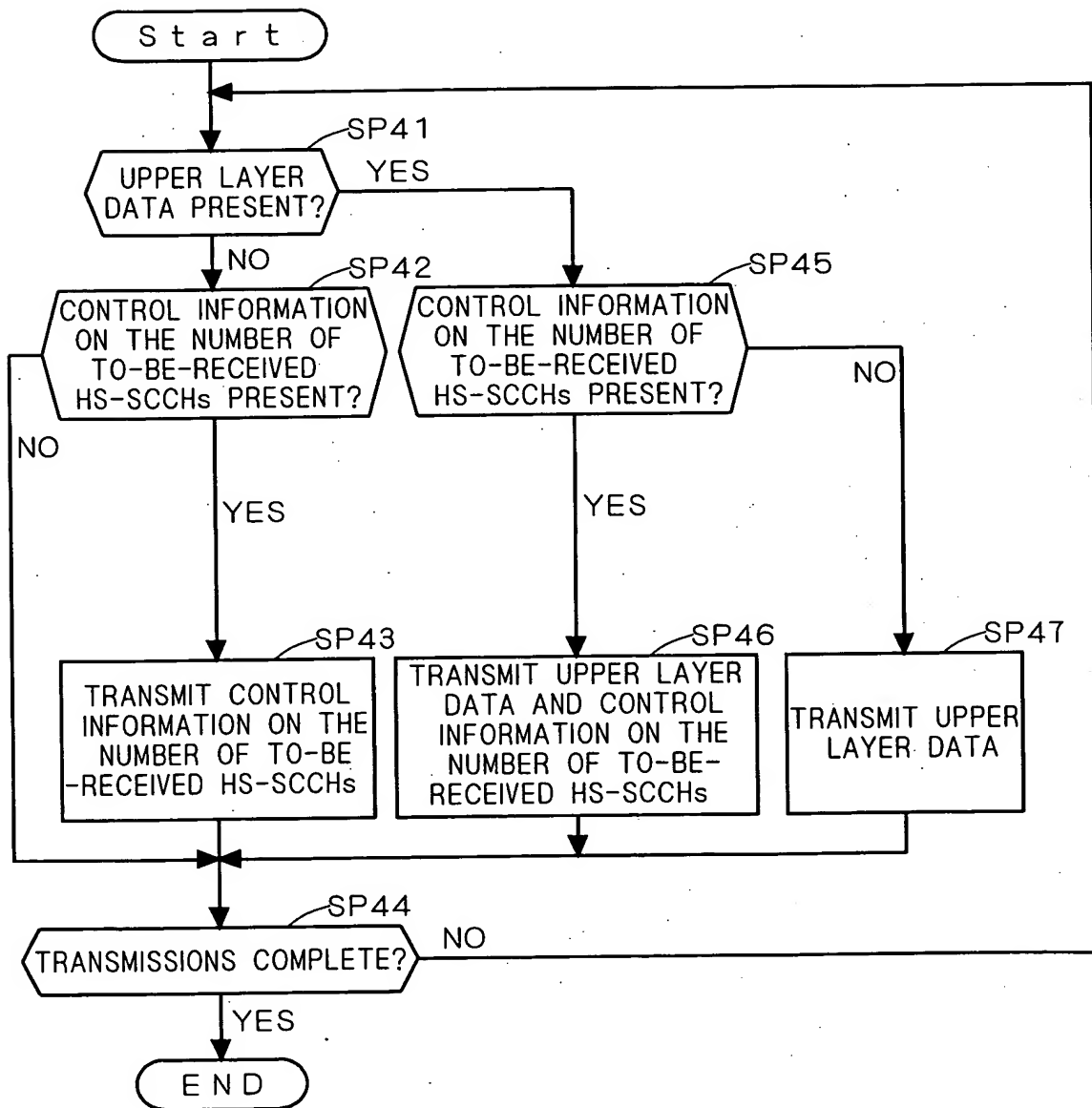
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FIG. 11



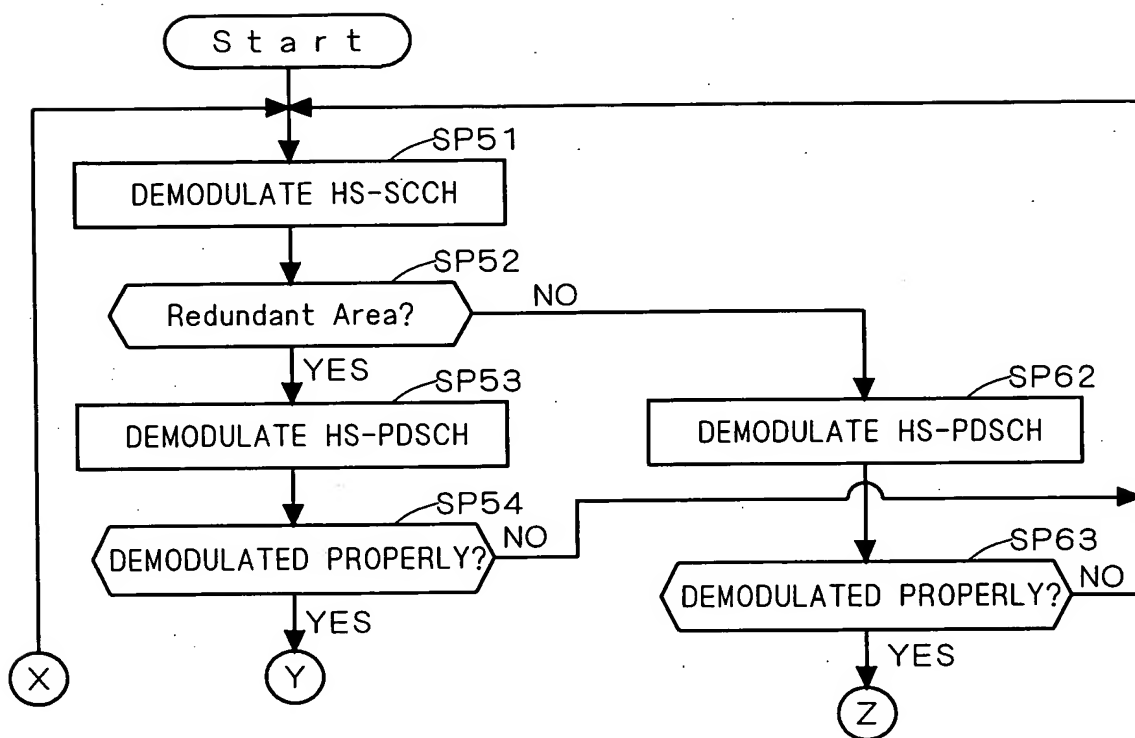
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FIG. 12



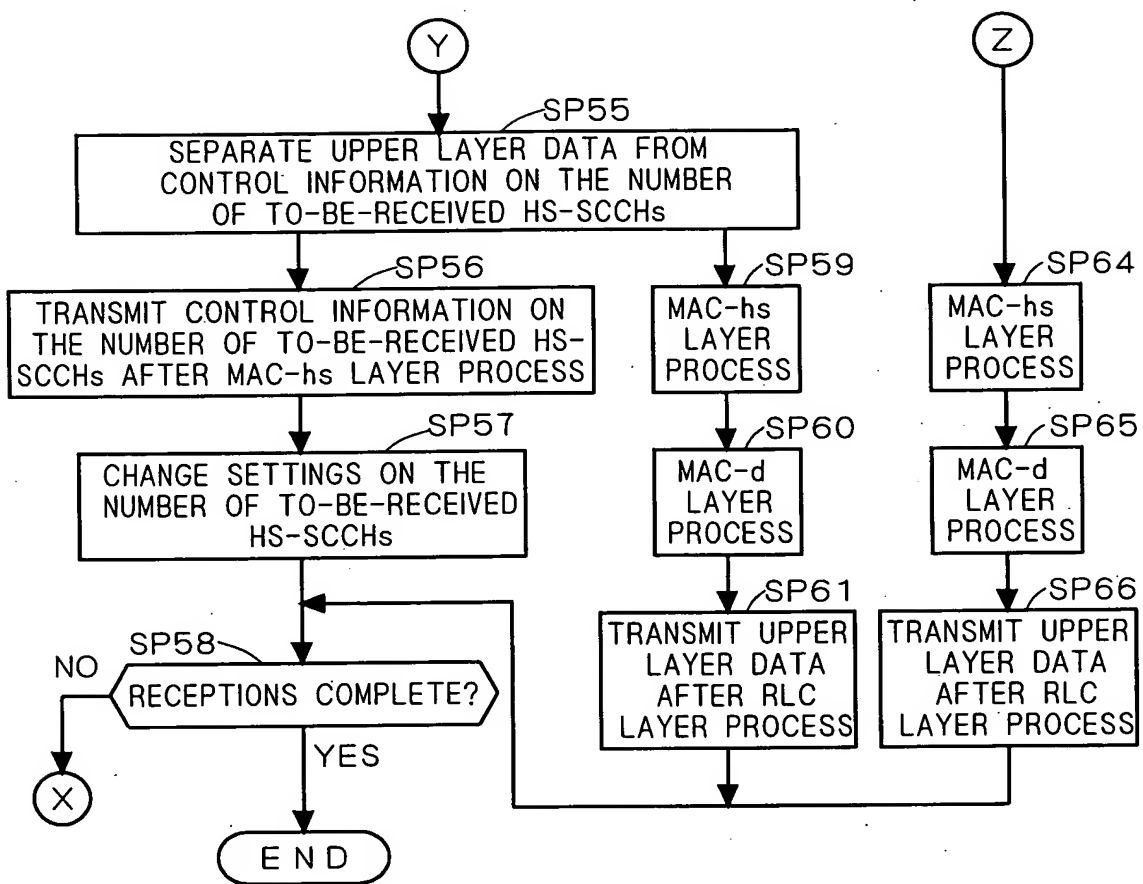
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FIG. 13



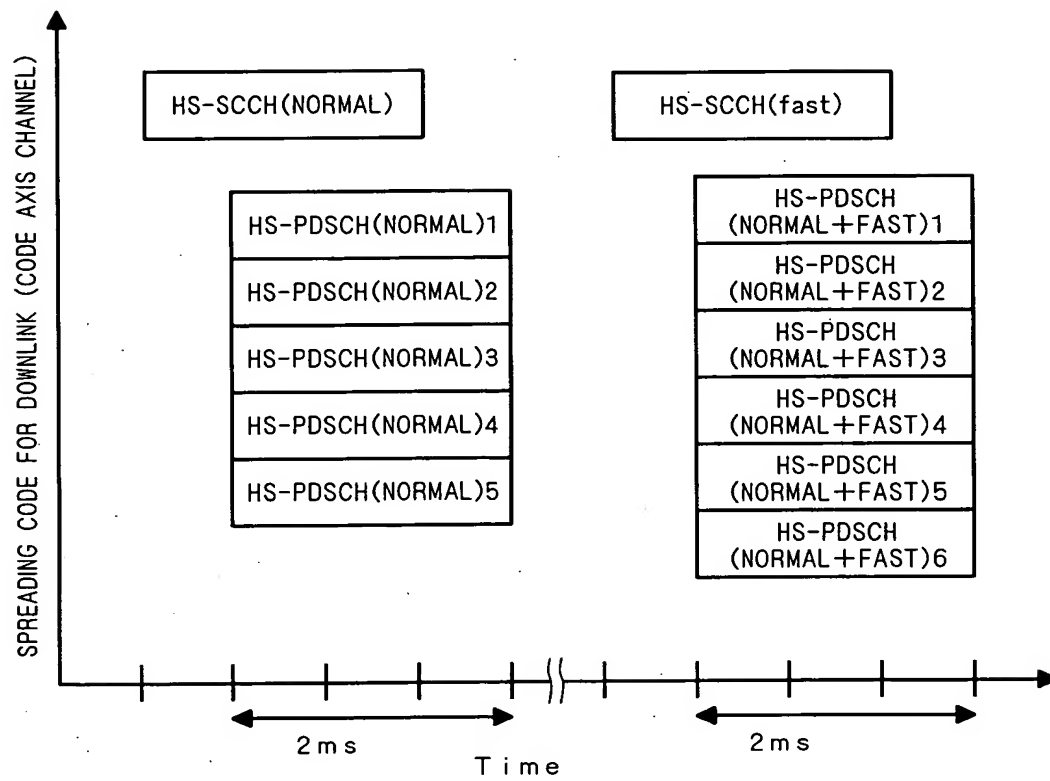
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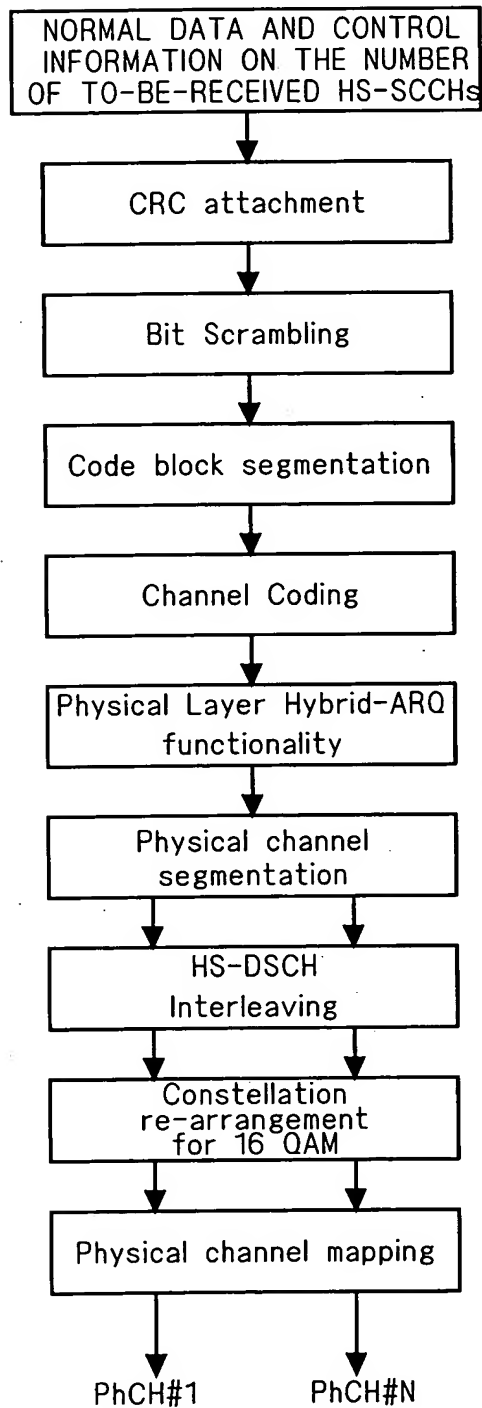
FIG. 14



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FIG. 15





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FIG. 17

- | | |
|---|--|
| - Channelization-code-set information(7 bits): | $X_{ccs,1}, X_{ccs,2}, \dots, X_{ccs,7}$ |
| - Modulation scheme information(1 bit): | $X_{ms,1}$ |
| - Transport-block size information
or Fast signalling notification (6 bits): | $X_{tbs,1}, X_{tbs,2}, \dots, X_{tbs,6}$ |
| - Hybrid-ARQ process information(3 bits): | $X_{hap,1}, X_{hap,2}, X_{hap,3}$ |
| - Redundancy and constellation version(3 bits): | $X_{rv,1}, X_{rv,2}, X_{rv,3}$ |
| - New data indicator(1 bit): | $X_{nd,1}$ |
| - UE identity(16 bits): | $X_{ue,1}, X_{ue,2}, \dots, X_{ue,6}$ |

